# SEP 0 8 2003 &

## **SCHIFF HARDIN & WAITE**

### PATENT DEPARTMENT

6600 SEARS TOWER

233 SOUTH WACKER DRIVE

CHICAGO, ILLINOIS 60606

re application of:

Heineck et al

Serial No.:

10/048,207

GROUP ART UNIT: 2812

Filed:

January 28, 2002

EXAMINER: C. Lattin

For:

SIR:

"METHOD FOR PRODUCING INTEGRATED SEMICONDUCTOR COMPONENTS"

CONFIRMATION NO.: 4793

SEMICONDUCTOR COMI ONENTS

AMENDMENT "B"

Commissioner for Patents
PO Box 1450

Alexandria, Virginia 22313-1450

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated as shown below

|                  | (2)<br>CLAIMS<br>REMAINING<br>AFTER<br>AMENDMENT   |       | (4)<br>HIGHEST NO.<br>PREVIOUSLY<br>PAID FOR | (5)<br>PRESENT<br>EXTRA | (6)<br>RATE                                 | OC<br>(7)<br>ADDITIONAL<br>FEE |
|------------------|--|-------|--|-------------------------|---|--------------------------------|
| TOTAL<br>CLAIMS  | * 10   | MINUS | **20   | x                       | ( ) X 9.00<br>( ) X 18.00                   |                                |
| INDEP.<br>CLAIMS | * 1  | MINUS | 3  | х                       | ( ) X 42.00<br>( ) X 84.00                  |                                |
|                  | mended to contain<br>dependent claims<br>paid for. |       |  | (') YES                 | ( ) \$140.00<br>( ) \$280.00<br>ONE<br>TIME |                                |

If the entry in Column 2 is less than the entry in Column 4, write "0" in Column 5.

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| _ | ••   |        | . ~      |           | C          | 1 1        |            |           |        |                 |

Applicants petition the Commissioner of Patents and Trademarks to extend this time for response to the Office Action dated \_\_\_\_\_ for \_\_\_\_ months so that the period for response is extended to \_\_\_\_\_\_. A check in the amount of \$\_\_\_\_\_ is attached to cover the cost of the extension. Any deficiency or overpayment should be charged or credited to deposit account No. 501519. A duplicate copy of this sheet is enclosed.

☐ A check in the amount of \$ \_\_\_\_ is attached.

☐ A check for \$ \_\_\_\_\_ accompanying IDS under 37 CFR 1.97(c) is attached

☐ A check for \$ \_\_\_\_\_ and Petition for Consideration of IDS under 37 CFR 1.97(d) is attached.

The Commissioner is hereby authorized to charge any additional fees which may be required, or to credit any overpayment to account No. 501519. A duplicate of this sheet is enclosed.

When phoning re this application, please call (312) 258-5781.

BY June M / Select (24,149

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450 on September 5, 2003.

| James D. Hobart              |
|------------------------------|
| NAME OF APPLICANT'S ATTORNEY |
| Rener II / Nelsus            |
| SIGNATURE                    |
| / September 5, 2003          |
| DATE                         |
|                              |



# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

# **AMENDMENT "B"**

APPLICANT:

Heineck et al

SERIAL NO.:

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GROUP ART UNIT: 2812

FILED:

January 28, 2002

EXAMINER: C. Lattin

INVENTION:

"METHOD FOR PRODUCING

CONFIRMATION NO.: 4793

INTEGRATED SEMICONDUCTOR

COMPONENTS"

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

SIR:

In response to the Office Action dated June 18, 2003, please amend the above-identified application as follows:

Amendments to the Substitute Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims that begins on page 3 of this paper.

Remarks/Arguments begin on page 5 of this paper.

TECHNOLOGY CENTER 2800